

**AMENDMENTS TO THE CLAIMS WITH MARKINGS TO SHOW CHANGES MADE,
AND LISTING OF ALL CLAIMS WITH PROPER IDENTIFIERS**

1.-26. (Cancelled)

27. (Currently amended) A motor control device, comprising:

- a control component adapted to provide a control signal;
- a signal dividing device adapted to divide ~~an amplitude of~~ the control signal into at least two signal portions, one of the control signal portions being a higher value signal portion and the other one of the control signal portions being a lower value signal portion;
- at least one signal processing device adapted to process each of the at least two control signal portions in different ways; and
- an adder device adapted to add together the differently processed control signal portions before further processing.

28. (Currently amended) The motor control device of claim 27 4, wherein the at least one signal processing device includes a low pass filter which is connected in a signal path for the lower value signal portion.

29. (Currently amended) The motor control device of claim 27 4, wherein the at least one signal processing device includes at least one band stop filter which is connected in a signal path for the lower value signal portion.

30. (Currently amended) The motor control device of claim 27 4, further comprising a position sensor for sensing a movement of an adjustment element; and an acceleration sensor for sensing a movement of the adjustment element.

31. (Currently amended) The motor control device of claim 27 4, further comprising a sampling device adapted to repeatedly sample a variable to be sensed within a time step so as to acquire a plurality of sampled values for the time step, said sampling device being adapted to supply an average of the sampled values acquired for the time step as an actual variable.
32. (Currently amended) The motor control device of claim 27 4, wherein the control component includes a subtraction device for providing a differential signal by subtracting an actual variable from a reference variable, said signal dividing device being connected downstream of the subtraction device.
33. (Currently amended) A method for controlling a motor, comprising the steps of:
 - providing a control signal;
 - dividing ~~an amplitude of~~ the control signal into at least two control signal portions, one of the control signal portions having a higher value signal portion and another one of the control signal portions having a lower value signal portion;
 - processing each of the at least two control signal portions in different ways; and
 - adding the differently processed control signal portions together before further processing.
34. (Previously presented) The method of claim 33, further comprising the step of filtering the lower value signal portion with a low pass filter.
35. (Previously presented) The method of claim 33, further comprising the step of filtering the lower value signal portion with at least one band stop filter.

36. (Previously presented) The method of claim 33, further comprising the steps of sensing a position signal; providing the position signal as an actual variable; sensing an acceleration signal; and providing the acceleration signal as an actual variable.
37. (Previously presented) The method of claim 33, further comprising the steps of repeatedly sampling a variable which is to be sensed within a time step so as to acquire a plurality of sampled values for the time step, and providing an average of the values acquired for the time step as an actual variable.
38. (Previously presented) The method of claim 33, wherein the providing step includes the step of subtracting an actual variable from a reference variable to produce a differential signal, said differential signal representing the control signal that is divided into the at least two control signal portions.
39. (New) The method of claim 33 wherein the high-value signal portion is processed to produce an I-component, wherein the lower-value signal portion is added to the I-component.
40. (New) The motor control device of claim 27 wherein the signal processing device is adapted to process the higher value signal portion to producing an I-component and the adder device is adapted to add the lower-value signal portion to said I-component.